

EEL 3701C: Digital Logic & Computer Systems

INSTRUCTOR Dr. Karl Gugel NEB 265 gugel@ufl.edu **Office Hours:** 9:00-9:30 and 2:45 - 4:00 pm, MWF

LECTURES

EEL3701	MWF 3 rd period (9:35 – 10:25 am)	NEB 202
EEL3701	MWF 7 th period (1:55 – 2:45 pm)	WM 100 (Williamson Hall)

LAB (NEB 248) SECTIONS

2G72 M3-5	05F7 M6-7	268H M10-11	0279 ME1-2	155F T4-5	5089 T6-7	1491 T10-11	174B TE1-2	2G94 W4-5	1088 W6-7
29HE W10-11	1488 WE1-2	9069 R2-3	5297 R 6-7	1496 R10-11	2557 RE1-2	5712 F3-4	155A F7-8	2G97 F9-10	

CATALOG DESCRIPTION

Elements of digital design with an emphasis on computer design; Boolean algebra, logic simplification, state machine design.

COURSE OBJECTIVES (ABET Design Content 50%)

The student will learn the functional and technological characteristics of microprocessor structures, memory components, peripheral support devices, and interface logic. Through laboratory experiments and textbook examples the student will learn how to integrate and apply microcomputer subsystems and components to common interfacing problems.

TEXTBOOK

Fundamentals of Logic Design, 5th Edition (or higher) by Charlie Roth, (ISBN#: 0534378048)

REFERENCES

Chapters 1-7 from H. Lam & J. O'Malley, *Fundamentals of Computer Engineering: Logic Design and Microprocessors, 1st edition* available at the University Copy Center.

OFFICE HOURS

My office hours are not flexible. If you can't attend them, see me between classes or visit a TA during their office hours.

REQUIRED SOFTWARE

Quartus - student version free from 'www.altera.com' to be further discussed in class.

REFERENCE MANUALS These will be given on our web page as well as other important technical documentation. Our web address is:

<http://www.add.ece.ufl.edu/3701/>

COURSE GRADE DETERMINATION

Exam #1*	28%
Exam #2*	28%
Laboratory**	26%
Homework	4%
Final Quiz	14%
Total	100%

* When exams are handed back for review in class, all re-grades for exams must be turned in at the end of the lecture period.

** A grade of 65% or better in lab alone is required in order to obtain at least a passing grade for the class. In other words, if you score below 65% in lab, it does not matter what your test or homework scores total, you automatically will fail the course.

WORKING TOGETHER - You are encouraged to work together on homework assignments and share ideas on lab assignments. However you are not allowed to copy or duplicate any lab material (code, drawings, etc.) from another student. This work will be considered cheating and you will automatically be failed from the course.

LABORATORY RULES

No food or drink in the Lab! Students work *individually* on each Lab project. It is the student's responsibility to return all equipment and clean her/his work area before leaving the Lab. Students must attend labs during their assigned time. Students must come prepared to the Lab. No student will be admitted to the Lab without the pre-lab work in hand.

Revision: July 25th

WEEK/DAY	DATE	HW	LAB #	Status	Comments
1	M	Aug 21		None	Syllabus & Number Systems
1	T	22		None	
1	W	23		None	Basic Logic & Number Systems
1	Th	24		None	
1	F	25		None	Basic Logic & Number Systems
2	M	28		1	Mixed Logic/Quartus Software/Number Systems
2	Tu	29		1	
2	W	30		1	Mixed Logic/Quartus Software/Number Systems
2	Th	31		1	
2	F	Sept 1	1	1	Mixed Logic/Quartus Software/Number Systems
3	M	4		None	No class Holiday
3	Tu	5		2	
3	W	6		2	Boolean Algebra/Mixed Logic/Circuit Design
3	Th	7		2	
3	F	8	2	2	Boolean Algebra/Mixed Logic/Circuit Design
4	M	11		2	MPOS/MSOP/Boolean Simplification
4	Tu	12		Workshop	
4	W	13		3	MSI Devices/K-Maps
4	Th	14		3	
4	F	15	3	3	MSI Devices/K-Maps
5	M	18		3	MSI Devices
5	Tu	19		3	
5	W	20		Workshop	MSI Devices
5	Th	21		4	
5	F	22	4	4	MSI Devices
6	M	25		4	Flip-Flops & Counters
6	Tu	26		4	
6	W	27		4	Flip-Flops & Counters
6	Th	28		None	
6	F	29	5	Workshop	Review
7	M	Oct 2		None	Exam #1 (estimated date due to room request)
7	Tu	3		None	
7	W	4		None	Registers/RAM/ROM
7	Th	5		Workshop	
7	F	6		None	No Class Homecoming
8	M	9		5	Exam #1 back in class
8	Tu	10		5	
8	W	11		5	ASM Design
8	Th	12		5	
8	F	13	6	5	ASM Design
9	M	16		Workshop	ASM Design
9	Tu	17		6	
9	W	18		6	ASM Design
9	Th	19		6	
9	F	20		6	RAM/ROM Expansion

Revision: July 25th

WEEK/DAY	DATE	HW	LAB #	Status	Comments
10	M	23	6		RAM/ROM Expansion
10	Tu	24	Workshop		
10	W	25	7		RAM/ROM Expansion
10	Th	26	7		
10	F	27	7		ALU Design
11	M	30	7		ALU Design
11	Tu	31	7		
11	W	Nov 1	Workshop		Fan-Out/Noise Margins
11	Th	2	Workshop		
11	F	3	8	Workshop	Review
12	M	6	None		Exam #2 (estimated date due to room request)
12	Tu	7	None		
12	W	8	None		Intro to G-CPU
12	Th	9	8		
12	F	10	None	No Class	Holiday
13	M	13	8		Exam #2 back in class
13	Tu	14	8		
13	W	15	8		G-CPU Design
13	Th	16	Workshop		
13	F	17	9	8	G-CPU Design
14	M	20	Workshop		G-CPU Design
14	Tu	21	Workshop		
14	W	22	None	No Class	Thanksgiving
14	Th	23	None		
14	F	24	None	No Class	Thanksgiving
15	M	27	9		G-CPU & Assembly
15	Tu	28	9		
15	W	29	10	9	G-CPU & Assembly
15	Th	30	9		
15	F	Dec 1	9		Review
16	M	4	None		Final Quiz (est. date due to room request)
16	Tu	5	None		
16	W	6	None	Optional	Verify any missing scores, Final Quiz back
16	Th	7	None		
16	F	8	None	No Class	End of Semester

Special Notes Relating to Lab:

- “Workshop” refers to an open lab intended to help students: (1) correct deficiencies from previous laboratory assignments; and/or (2) get an early start on the next laboratory assignment. Although students assigned to each designated section(s) of the workshop have priority, students from other sections may attend outside their own on a space-available basis.
- Students with similar designs (copies) will both receive a zero on the lab. Depending on the level of infraction, you may be automatically failed in the class. Erase your designs on the lab computer before you leave lab to prevent others from using your work!
- Students will be allowed a one week make-up pass on one lab for full credit. This pass covers all labs missed for medical, accidental, emotional and any other reason a lab was missed. One pass/One lab per student. This does not include Lab #9 which needs to be completed at your regularly scheduled time due to time constraints at the end of the semester.

Special Notes Relating to the Two Exams & Final Quiz:

- All syllabus dates for exams & the final quiz are subject to change due to the room request procedure. i.e. I have asked for the dates shown, however the registrar’s office may assign us a room on a different (but close) date.